IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned Examiner: To Be Assigned

In Re PATENT APPLICATION Of:

Applicants : Yoshinori SHIZUNO)
Serial No. : To Be Assigned)
Filed : November 7, 2003) INFORMATION
For : SEMICONDUCTOR DEVICE)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Attached are copies of two Japanese publications and one English-language European patent publication. Any relevance of the Japanese publications can be gleaned from the attached English-language Abstracts. The publications are listed on the attached Form PTO-1449.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the documents submitted. Consideration of the submitted and listed documents is respectfully requested.

Respectfully submitted,

November 7, 2003

Date

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FORM PTO-1449				Atty Docket		Application No.			
INFORMATION DISCLOSURE STATEMENT				OKI 380		To Be Assigned			
			Applicant						
				Yoshinori SHIZUNO					
				Filing Date		Group Unit			
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U.S. PATENT DOCUMENTS									
Examiner Initial		Document Number	Date	Name		Class	Sub- Class	Filing Date	
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FOREIGN PATENT DOCUMENTS									
		Document Number	Date	Country		Class	Sub- Class	Trans- lation	
	AK	JP 10-256417	09/25/98	Japan				Abstract	
	AL	JP 11-219984	08/10/99	Japan				Abstract	
	AM	EP 0915505	05/12/99	Europe			4		
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OTHER (Including Author, Title, Date, Pertinent Pages, etc.)									
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Examiner	Examiner Date Considered								
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.									